

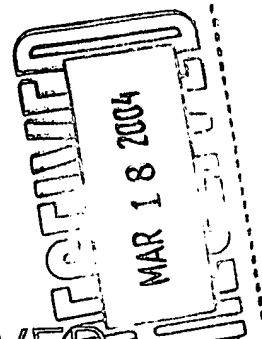
Attorney's Docket No.: 10559-601001/P12885

2185

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al.
Serial No.: 09/942,116
Filed : August 29, 2001
Assignee : Intel Corporation
Title : LOGIC SIMULATION

Art Unit: 2185
Examiner:



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Technology Center 2100

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This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

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Applicant : William R. Wheeler et al.
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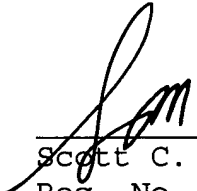
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earnestly solicited along with an early action on the merits.

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Respectfully submitted,

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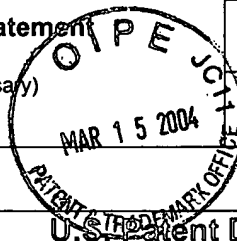


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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001	Application No. 09/942,116
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date August 29, 2001	Group Art Unit 2185



U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	RE 38,059	04/2003	Yano, et al.			
	AB	4,703,435	10/1987	Darringer, et al.			
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Examiner Signature				Date Considered			
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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(37 CFR §1.98(b))

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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ANNN	0 404 482	12/1990	EPO				
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	AVVV	08-314892	11/1996	Japan				
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	AYYY	WO 98/55879	12/1998	WIPO				
	AZZZ	WO 99/39268	08/1999	WIPO				

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Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
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		Group Art Unit 2185	Technology Center 2100

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AAAAA	WO 00/65492	11/2000	WIPO				

Other Documents (include Author, Title, Date, and Place of Publication)		
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	ALLLL	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

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